

REMARKS

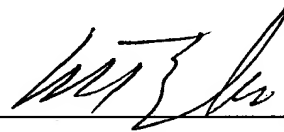
This is in full and timely response to the Notice of Non-compliance issued on Feb. 12, 2004.

As noted above, in accordance with 37 CFR § 1.121(h) only the sections which require amendments are included in this response. Inasmuch as the two sections which require amendment are the listing of the claims and the showing of the amendments which have been made to the abstract, are at issue, it is submitted that this response overcomes the objections raised.

Respectfully submitted,


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By



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AMENDED ABSTRACT

ABSTRACTEXTERNAL FAULT TOLERANT SHARED MEMORY UNIT IN A DISTRIBUTED
MULTIPROCESSING SYSTEMABSTRACT

A distributed multiprocessing system, comprises several hosts (2, 3, 4) connected to a network (1); each Each host has a processing unit (21, 31, 41) and internal memory (22, 32, 42) accessed by the processing unit; in In addition, each host further has an access device (24, 34, 44) connected to a fault tolerant external memory unit (6) by a fast connection (25, 35, 45). Each processing unit accesses the external memory unit transparently. [[¶]] The processing units of the distributed multiprocessing system thus share the same view of the external memory unit. This makes it possible to use symmetric multiprocessing memory sharing mechanisms, in a distributed multiprocessing system, due to the reduction of latency overheads by 4 or 5 orders of magnitude compared with standard DMP interconnect methods.

Figure 1.